



GOLDEN GATE

I N V I T A T I O N A L

2020 Golden Gate Invitational Tournament

Circuit Lab C

Exam Booklet

- DO NOT BEGIN UNTIL GIVEN PERMISSION
- You will have **50 minutes** to complete the exam || You **may** separate the exam
- For calculation questions, it is **not** required that you show your work, however partial credit will be assigned if correct steps are shown with an incorrect answer.
- Answers must be given with appropriate **significant figures** and **units** to receive full credit.
- All final answers must be placed inside the designated box, including multiple choice.
- **Lab:** You will have up to **20 minutes** to complete **each** lab section (**2 total**). A proctor will instruct you when it is your turn. You will not be given any replacement components, so be careful not to damage them.
- **Allowed materials:** 3-ring binder, writing utensils, two calculators, basic multimeter
- **Tie-breaker order:** 5, 12, 20, 23, 27, 30, 31, 34, Lab 2

Competitors: _____

School Name: _____

Team Number: _____

Rank: _____

Score: _____

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Page Number	Possible Score	Your Score
3	20	
4	22	
5	24	
6	26	
7	20	
8	22	
9	29	
LAB 1	41	
LAB 2	34	
Total	238	

11. What is the fastest Analog-to-Digital converter (ADC)? ¹ (2 pts)

A. Digital Ramp ADC

B. Successive Approximation ADC

C. Flash ADC

D. Sigma-delta ADC

12. Which of the following correctly describes an electrostatic interaction? ¹ (2 pts) (TB#2)

A. Two positive charges have an attractive force

B. Two negative charges have an attractive force

C. One positive and one negative charge will attract

D. One positive and one negative charge will repel

13. A Wheatstone bridge can NOT be used to indirectly measure which of the following? ¹ (2 pts)

A. Force

B. Temperature

C. Pressure

D. Time

14. Which of the following cause hysteresis in ferromagnetic materials? ¹ (Select all that apply) (4 pts)

A. Rotation of magnetization

B. Change in size of magnetic domains

C. Change in temperature

D. Change in pressure

15. Which of the following describes the velocity (v) and acceleration (a) of a small sphere of charge $+q$ when placed near a large, fixed sphere of charge $+Q$? It will move away from the large sphere with: ¹ (2 pts)

A. Decreasing v , constant a

B. Decreasing v , increasing a

C. Increasing v , constant a

D. Increasing v , decreasing a

16. How much energy is dissipated as heat in 10 seconds by a $100\ \Omega$ resistor with a $0.5\ \text{A}$ current? ¹ (2 pts)

A. 25 J

B. 100 J

C. 250 J

D. 500 J

17. For an ohmic conductor, doubling the voltage without changing the resistance will cause the current to? ¹ (2 pts)

A. Decrease by a factor of 4

B. Decrease by a factor of 2

C. Increase by a factor of 2

D. Increase by a factor of 4

18. A hollow metal sphere is placed inside an electric field. An electron is then placed at the center of the sphere. In what direction will the electron move? ³ (2 pts)

A. In the direction of the electric field

B. Opposite of the electric field

C. Perpendicular to the field

D. None of the above

19. Assuming all other conditions are equal, which of the following scenarios would you expect a human to have the lowest amount of body capacitance? ³ (2 pts)

A. Standing on top of the Golden Gate bridge on an arid afternoon

B. Leaning against the bottom of a grounded 20 ft metal pole in humid conditions

C. Body capacitance is the same in all scenarios

D. Leaning against a metal shed wall on an arid night

20. Consider a simple closed circuit with a battery and a resistor. If the battery is $12\ \text{V}$ and the current flowing through the circuit is $1.0\ \text{mA}$, what are the first three band colors of the resistor? ³ (2 pts) (TB#3)

A. Brown, Red, Red

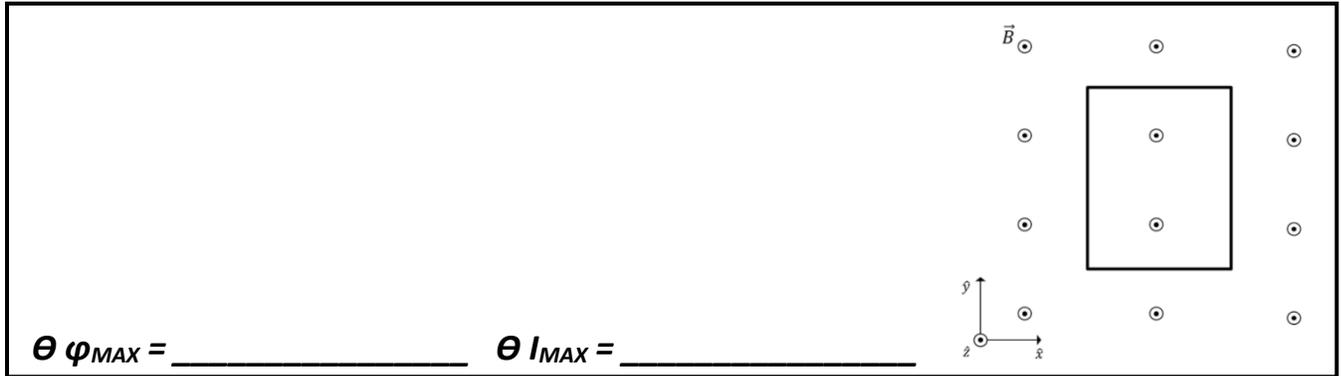
B. Brown, Red, Orange

C. Red, Black, Red

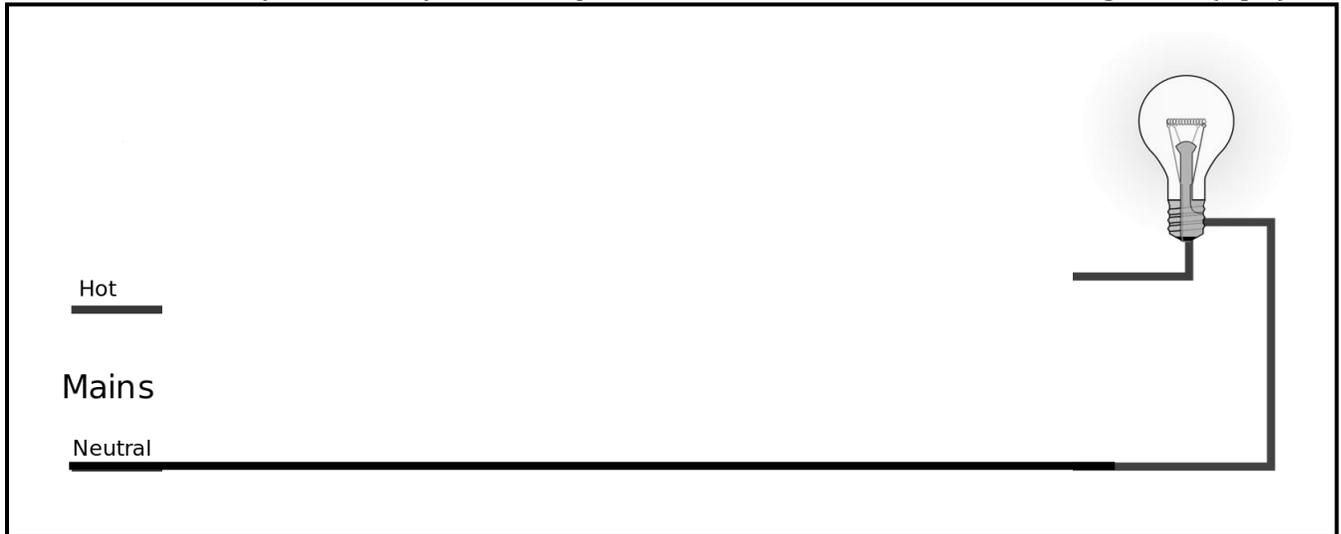
D. Red, Black, Orange

21. A rectangular loop is placed in a constant magnetic field region as shown below. The magnetic field region is initially perpendicular to the loop's plane. The loop is then rotated about an axis running through its center in the y-direction such that the angle of the loop's surface to the direction of the magnetic field region varies. Perform the following: ³ (6 pts; 3, 3)

- Determine the angle of the loop's surface to the direction of the magnetic field region that gives the largest amount of magnetic flux through the loop. (3 pts)
- Determine the angle, if any, between the loop's surface and the direction of the magnetic field region that gives the largest amount of current running through the loop. (3 pts)

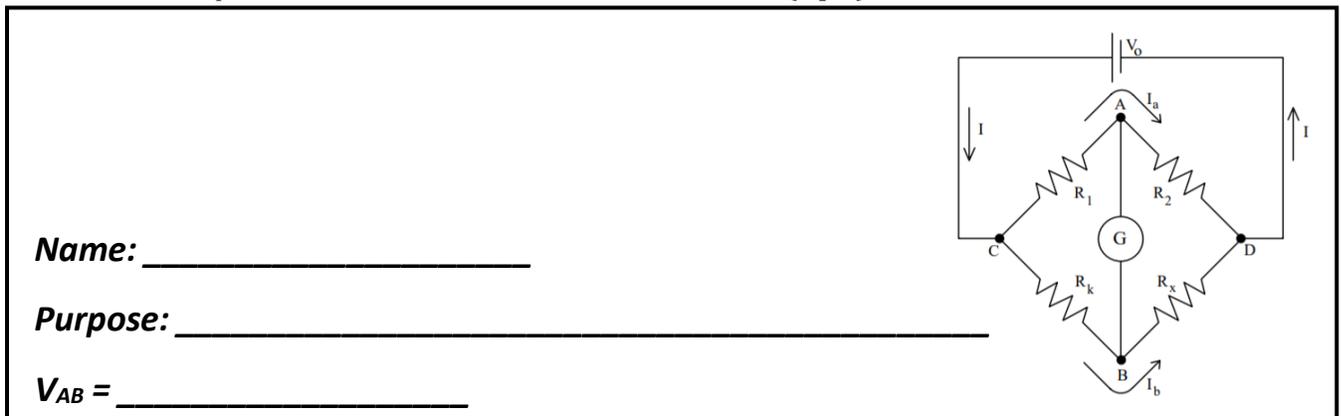


22. Draw a California 3-way connection system to complete the circuit between the hot line and the light bulb. (6 pts)



23. In the circuit shown, R_k and the ratio of R_2/R_1 are known. R_x is not known. Perform the following: ¹ (12 pts) (TB#4)

- State the name of the circuit. (2 pts)
- State the purpose of the circuit. (2 pts)
- State an equation for the V_{AB} in terms of V_o , R_k , R_x , R_1 , and R_2 . (8 pts)



24. Determine the power dissipated by R1, R3, and R4. Provide your answers to 3 significant figures. ¹ (6 pts; 2, 2, 2)

$P_{R1} = \underline{\hspace{2cm}}$ $P_{R3} = \underline{\hspace{2cm}}$ $P_{R4} = \underline{\hspace{2cm}}$

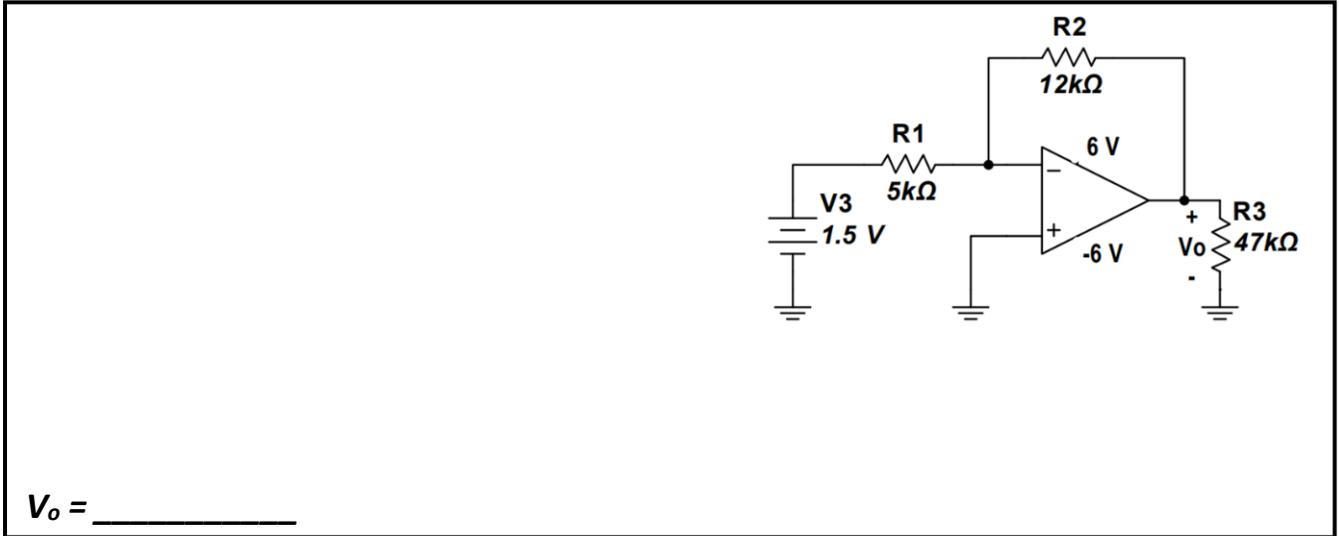
25. Determine the current i thru resistor R1 and the voltage v across the dependent source. Provide your answers to 3 significant figures. ¹ (12 pts; 6, 6)

$i = \underline{\hspace{2cm}}$ $v = \underline{\hspace{2cm}}$

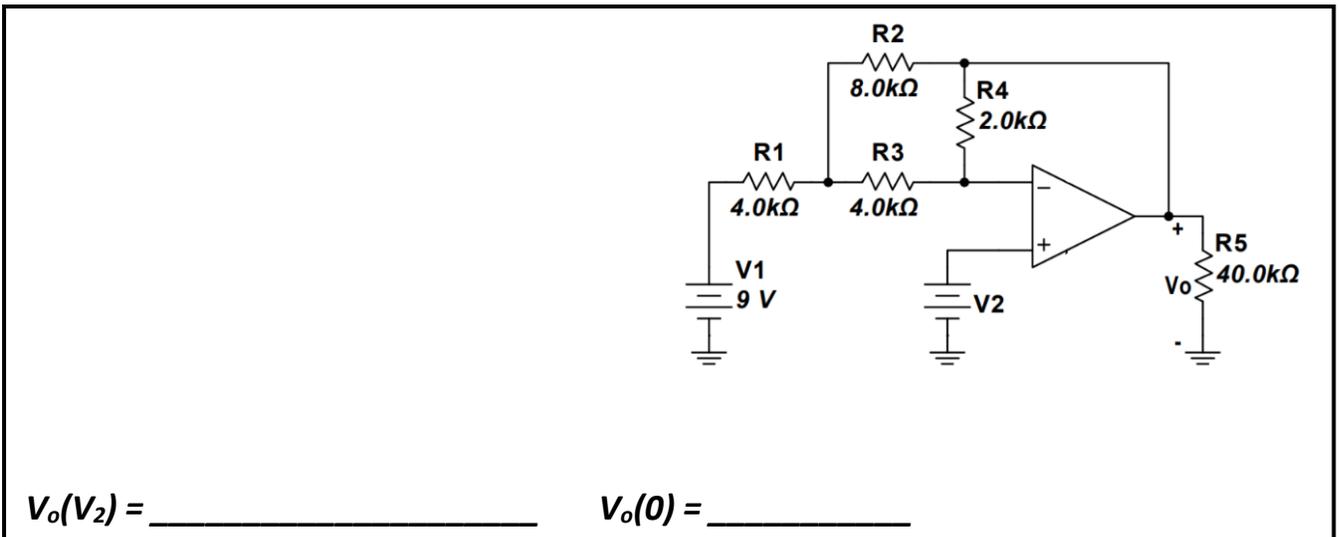
26. Determine the voltages V_1 and V_2 in the circuit below. Provide your answers to 3 significant figures. ¹ (8 pts; 4, 4)

$V_1 = \underline{\hspace{2cm}}$ $V_2 = \underline{\hspace{2cm}}$

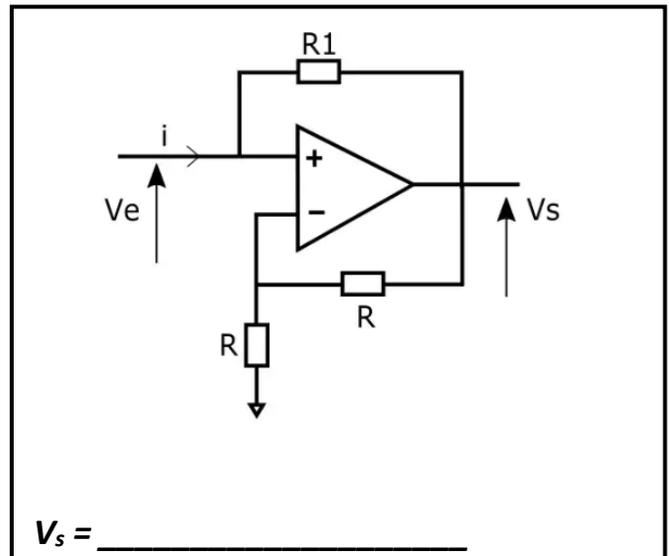
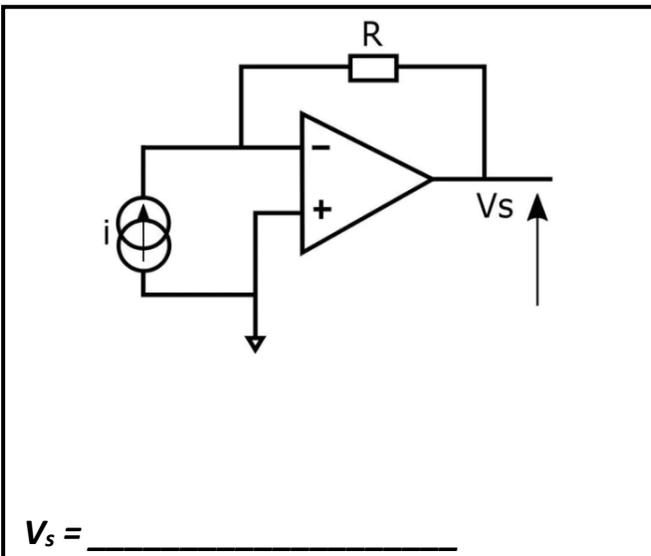
27. Determine V_o . Provide your answer to 3 significant figures. ¹ (6 pts) (TB#5)



28. Write V_o in terms of V_2 . Then determine V_o when $V_2 = 0$ V. Provide your answers to 3 significant figures. ¹ (8 pts; 6, 2)



29. Determine expressions for V_s in terms of the variables present in each circuit diagram shown. ¹ (6 pts; 3, 3)



30. Shown in the circuit diagram below is a 2-bit flash Analog-to-Digital Converter (ADC). Given that $V_{REF} = 3.0\text{ V}$, determine the following: ¹ (10 pts; 2, 4, 2, 2) (TB#6)

- Determine the Least Significant Bit (LSB) and Most Significant Bit (MSB). (2 pts)
- Provide the binary outputs (B_1 and B_2) for each of the given input (V_{IN}) in the table below. (4 pts)
- What is the resolution of the flash ADC shown below? (2 pts)
- Most practical ADCs require 8-bits. How many comparators would be needed for an 8-bit flash ADC? (2 pts)

Circle one for each:

LSB = (B_1 || B_0)

MSB = (B_1 || B_0)

Resolution = _____

Comparators needed = _____

V_{IN}	B_1	B_0
0 V		
1 V		
2 V		
3 V		

31. Solar cells can be modelled by the circuit shown in the diagram below. $R_{SH} = 0.1\text{ m}\Omega$, $R_S = 5.0\text{ }\Omega$, and $I_D = 251\text{ }\mu\text{A}$. Note that all of the elements in the circuit are operating under ideal conditions at room temperature (300 K). Perform the following: ³ (8 pts; 2, 2, 4) (TB#7)

- Describe the purpose of the shunt resistor R_{SH} in the circuit. (2 pts)
- Describe the purpose of the flyback diode in the circuit. (2 pts)
- Suppose you attach an LED to the open terminals shown that requires 1.8 V to turn on. Will the LED turn on? Assume that the saturation current of the diode is 10^{-12} A . (4 pts)

R_{SH} : _____

Flyback Diode : _____

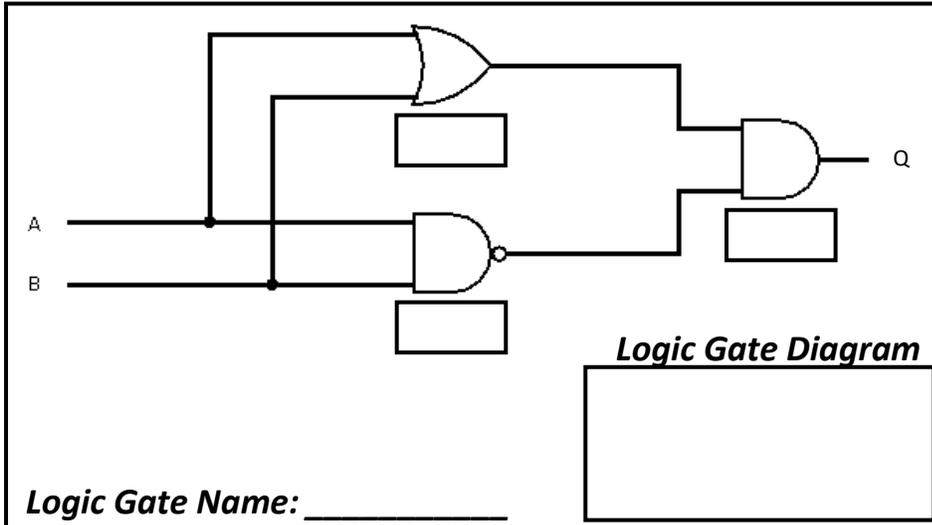
LED turns on? (Circle one): (YES || NO)

32. Briefly explain how light is created by an LED when a forward-biased voltage is applied. ¹ (4 pts)

LAB 2 (Electrical Control): At this station, you will **analyze** and **construct** a circuit. Below these instructions is an image of a digital logic gate. Provided at this station is a breadboard and the necessary components for you to build the logic gate circuit. ¹ **(34 pts total) (TB#9)**

38. **[Analysis]** Perform the following for the digital logic diagram pictured: ¹ **(12 pts)**

- **Label** the three logic gates. **(3 pts)**
- **Complete a truth table** for the circuit using the template provided. **(4 pts)**
- The three logic gates that compose this circuit can be replaced with a single logic gate. **Name** that gate. **(2 pts)**
- **Draw** the single logic gate that can replace this circuit. Label A, B, and Q. **(3 pts)**



A	B	Q
0	0	
1	0	
0	1	
1	1	

39. **[Construction]** Using the components provided, build a digital logic circuit that obeys the truth table pictured above. There are many solutions to this problem; you will receive full credit as long as your circuit meets the criteria specified below. Individual gates do not need to be represented. **Call over a proctor to verify your circuit functions correctly before time is up. (22 pts)**

- The circuit has the correct output for all combinations of input A and B. **(12 pts)**
- NPN transistors and resistors are used correctly. **(4 pts)**
- Push buttons comprise inputs A and B. **(4 pts)**
- An LED is used to represent the output. **(2 pts)**

Proctor Initials = _____